

AMENDMENTS TO THE SPECIFICATION:¹

Please replace paragraph [0004] with the following rewritten paragraph:

--[0004] The following description of a conventional terminal arrangement makes reference to Fig. 12. Circuit board 500 has a first terminal row with a ground terminal 510 in its center, a data I/O terminal 520 to a first side thereof, and a read/write signal terminal 530 to a second side thereof; and a second terminal row, located above the first terminal row, with a power terminal 540550 in its center, a clock signal terminal 550540 to a first side thereof, and a chip select signal terminal 560 to a second side thereof.--

Please replace paragraph [00077] with the following rewritten paragraph:

--[00077] The following description of a third ~~second~~-embodiment makes reference to Fig. 9. The circuit board 300 pertaining to the third embodiment is provided in the upper half of its juxtaposed face 13 with a substantially circular test terminal 20 used to test the storage device 30 when shipped from the factory, and in the lower half of its juxtaposed face 13 with a plurality of circular terminals 301 -306, arrayed randomly. The circular terminals are, proceeding from the left in the drawing, a ground terminal 301, a read/write terminal 302 for inputting read/write control signals for storage device 30, a clock terminal 303 for inputting a clock signal (sync signal) for storage device 30, a power supply terminal 304 for supplying power, an I/O terminal 305 for data input/output, and a ground terminal 306.--

¹ This Amendment uses the paragraph numbering of the specification as originally filed, not the numbering in the disclosure published on March 11, 2004.